DESIGNCON[®] 2024 WHERE THE CHIP MEETS THE BOARD

JANUARY 30 - FEBRUARY 1, 2024

Santa Clara Convention Center Santa Clara, CA



2023 Attendee List At-A-Glance

Company	Job Title	
3M	Sr. Product Development Specialist	
3M	Product Manager	
A&D	Product Manager	
Accelink USA Corp.	VP R&D	
Accton Technology	SVP, R&D	
Ace Unitech Inc	CEO	
Achronix Semiconductor Corp.	Senior Principal Engineer	
Active Electrical Cables (AEC)	Senior Hardware Engineer	
AdiCal Global Incorporated	CEO	
Advantest Corporation	Principal Engineer	
AEi Systems	Senior Engineering Scientist	
AGC Multi Material America	Vice President	
AGP eGlass	Senior Scientist	
Agrimation	Design Engineer	
Ai4Go	CFO	
AKMMV USA Inc.	Director, FAE	
Aleano	Software Engineer	
Allion USA-SGS	President	
Alphawave Semi	Vice President	
Altaflex	Vice President	
Altair Engineering	Sr. Application Engineer	
Altamont Technical Services	Vice President	
Amazon	Engineering Manager	
Amazon	Hardware Development Engineer	
Amazon	Sr. FPGA Design Engineer	
Amazon	Sr. Signal Integrity Engineer	
Amazon/Lab 26	Sourcing Manager	
AMD	Sr. Manager Silicon Design Engineer	
Amotech	Director of Product Strategy	
Ampere Computing	Principal Packaging & Systems Engineer	
Amphenol	Senior Staff R&D Signal Integrity Engineer	
Amphenol	Field Applications Engineer	
Analog Devices	Principal Engineer	
Analog Devices	Senior PCB Designer	
Anritsu	MMIC Design Engineer	
Anritsu	Sr. Architect	
Ansys, Inc.	Vice President	

Company	Job Title	
Ansys, Inc.	Distinguished Engineer	
Apex Semiconductors	Distinguished Engineer	
Apex Semiconductors	Vice President	
AplusRF	CEO	
Apolent	Partner	
Apple	Mac System Technologies Engineer	
Apple	Hardware Engineer	
Apple	SI/PI Engineer	
Apple	PCB Technologist	
Applied Motion Products	Direct OEM Sales Manager	
Archband Inc.	President	
Arista Networks	PCB Technologist	
Arista Networks	Supplier Quality Engineer	
Astera labs	Chief Technologist	
Astera Labs	Vice President	
Averatek Corporation	VP, Manufacturing	
Averatek Corporation	VP, Engineering	
Avicena Tech	CEO	
Aviva Links	000	
AVX	Chief Scientist	
Biotricity Inc.	VP of Engineering	
BlackBerry	Field Application Engineer	
BMW	In-Vehicle System Connectivity HW Design & Development Engineer	
Bosch	Director	
Bosch	System Engineer, E/E-Architecture Communication Networks	
Boston Dynamics	Associate Director of Electrical Research and Development	
Broadcom	Sr. Manager IC Design Engineering	
Broadcom	R&D Director	
Cadence	Vice President of R&D for High- Performance PHY	
Cadence	Corporate Vice President	
Caltron Components	Vice President	
Cerebras Systems	Vice President, Product Management	
Cisco	Senior Director of Engineering	
Cisco	Mechanical Product Engineer	
Cisco	Design Engineer	

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#1 Attendee Priority:

See new products/innovations in-person

Company	Job Title
Cisco	Principle Hardware Engineer
Cruise LLC	Director of Hardware Engineering
Dasssault Systemes	Software Development Manager
Dell Technologies	Senior Principal Engineer
Dell Technologies	Distinguished Engineer
Dell Technologies	Hardware Architect
Digi-Key Electronics	Senior Product Manager
DISH Network	Sr. Manager, Hardware Engineering
DuPont	Applications Engineering Leader
Dupont	Lead NBD manager
DuPont	Principal Engineer
Ericsson	Data Scientist
Everactive	VP, Ecosystem Development
Ford Motor Company	Quality Software Engineer
-UJI Corporation	Engineer
uturewei	Sr. Vice President
GE Healthcare	Hardware Electrical Engineer
Google	Chip Package Design Architect
Google	Senior Signal Integrity and Power Integrity Engineer
Google	Principal Engineer
ioogle	Thermal and Systems Engineer
SI Technology	VP Engineering
lenkel	Application engineer
lewlett Packard Enterprise	Electrical/Hardware Engineer
IKAM America, Inc.	President
lirose Electric USA	Optical Signal Integrity Engineer
lirose Electric USA	HS Engineering Manager
oneywell	Sr. Advanced Electrical Engineer
Ioneywell Aerospace	Electrical Engineer
luber+Suhner, Inc.	Principal Development Engineer
lyundai KEFICO Corporation	Senior Research Engineer
BM	Senior Engineer
nfineon Technologies AG	Lead Principal Engineer
ntel	Principal Engineer/Manager, Programmable Hardware Engineering
ntel	Director of Quantum Hardware
ntel	Head of Silicon Photonics Strategy, Intel Foundry Services

Company	Job Title	
Intel	Director of Electrical Validation/ SOC Engineering	
Isola Group	VP of Global Applications	
Japan Aviation Electronics	Engineering Manager	
John Deere	Sr. Electronic Design Engineer	
Johns Hopkins APL	Electrical Engineer	
Johnson & Johnson Vision	Manufacturing Engineer	
Juniper Networks	Signal Integrity Engineer	
Juniper Networks	PCB Technologist	
Keysight Technologies	Senior System Architect	
Keysight Technologies	Vice President and General Manager	
KLA Corporation	Sr. Electrical Design Engineer	
Kura Technologies	CEO and Founder	
Leadman Electronics	Buyer	
Luxshare-ICT	Component Engineer	
Marvell	VP of Technologies	
Marvell	Sr. Principal Engineer	
Marvell	Application Engineer	
Marvell	Electrical Validation Engineer	
MathWorks	Senior Application Engineer	
Mayo Clinic	Senior Engineer	
Mentor, A Siemens Company	Principal Product Architect	
Meta	SIPI EMC Manager	
Meta	Power Integrity Engineer	
Meta	Mechanical Engineering Manager	
Meta	GSM	
Microchip	Technical Fellow	
Microsoft	Director Semiconductor and EDA	
Microsoft	Principal EE Architect	
Microsoft	Principal Architect	
Microsoft	Sr. Manager, Strategic Sourcing	
Molex	Design Engineering Manager	
Molex	Senior SI Design Engineer	
Molex	RF Design Engineer	
Nokia	Hardware Engineer	
Northrop Grumman	Principal Engineer Digital	
Northrop Grumman	Technical Fellow and Staff Digital Engineer	
Nvidia	System Architect	

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89% of attendees recommend, specify, or make the final buying decision

Company	Job Title	Company	Job Title
Nvidia	Senior Director of Sourcing	Siemens	Software Engineer
NXP Semiconductor	Technical Director	Sony Electronics Inc.	Sr. Engineer
NXP Semiconductor	Applications Engineer	Sony Electronics Inc.	Director
Omron Robotics and Safety Technologies	Electrical Engineer	Stanford University	Systems Engineer
O-Net Communications	Senior Scientist	Stryker	Staff Engineer
Oracle	Sr. Hardware Engineer	Stryker	Sr. R&D Staff Engineer
Oracle	Software engineer	Supermicro	Engineering VP
Oracle	Component Engineer	Supermicro	Sr. Director, Solutions Architec
Pactech	Computer Engineer	Supermicro	Senior Buyer
Palo Alto Electron	CEO	Synaptics	Hardware Design Engineer
Palo Alto Networks		Synaptics	Sr. Staff Systems Engineer
Palo Alto Networks	Signal Integrity Engineer	Synopsys	Silicon Validation Engineer
	Component Engineering	Synopsys	VP, Engineer
Panasonic Panasonic	Product Devlopment Manager	TE Connectivity	Sr. Product Development Engl
	Application Engineering Manager	TE Connectivity	Principal Scientist
Picotest	Operations Manager	Tektronix	Cisco Hardware Engineer
Qualcomm	Senior Director	Tektronix	Google Hardware Engineer
Qualcomm	Engineering manager	Tesla	Hardware Engineer
Qualcomm	Sr. Staff Engineer	Tesla	Sr. Mechanical Design Engine
Rambus	AMTS Validation Engineer	Tesla	Silicon Develop Engineer
Rambus	Fellow and Distinguished Inventor	Tesla	Lead SIPI Engineer
Rohde & Schwarz	Characterization Engineer	Teledyne LeCroy	VP, Technology Development
Rohde & Schwarz	Application Engineer	Teledyne LeCroy	VP Product Development
Roku Inc.	Sr. Hardware Engineer	Teradyne, Inc.	Systems Engineer
Rosenberger Hochfrequenztechnik GmbH & Co. KG	Principal Expert Signal Integrity	Teradyne, Inc.	Hardware engineer/technical l
Rosenberger Hochfrequenztechnik GmbH & Co. KG	Director R&D / Head of Automotive R&D	Texas Instruments	Systems Engineer
SambaNova Systems	Principal Hardware Engineer	Texas Instruments	Design Engineer
•		Texas Instruments	Validation Engineer
SambaNova Systems	Principal Engineer	Uber	Sr. Manager, Infrastructure
Samsung	Vice President Senior Principal Engineer	Uber	Sr. Technical Program Manage Hardware
Samsung	Director of Product Planning	Western Digital	Signal Integrity and EMC
Samsung Semiconductor	VP Engineering	Western Digital	Technologist
Samtec	Senior Signal Integrity Design Engineer	Western Digital	Distinguished Engineer, Sr. Director
-	Senior Principle Engineer and	Xconn Technologies	VP, Systems Engineering
Samtec	Leading Principal SI/PI Architect	Xilinx	Staff Design Engineer
Sandia National Laboratories	Electrical Engineer	Xpeedic	Principle Engineer
Sandia National Laboratories	Computer Engineer	Yamaichi Electronics Inc.	Chairman, CEO
Semtech	Sr. Director of IoT Solutions	Yamaichi Electronics Inc.	Mechanical Design Engineer
Siemens	Multi-physics Solution Architect	Zoom	Electrical Test Engineer

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